

General Description

The MAX16800 current regulator operates from a 6.5V to 40V input voltage range and delivers up to a total of 350mA to one or more strings of high-brightness LEDs. The output current of the MAX16800 is adjusted by using an external current-sense resistor in series with the LEDs. An enable input allows wide-range "pulsed" dimming. Wave-shaping circuitry reduces EMI. The differential current-sense input increases noise immunity. The MAX16800 is well suited for applications requiring high-voltage input and is able to withstand automotive load-dump events up to 40V. An on-board pass element minimizes external components while providing ±3.5% output current accuracy. Additional features include a 5V regulated output and short-circuit and thermal protection.

The MAX16800 is available in a thermally enhanced, 5mm x 5mm, 16-pin TQFN package and is specified over the automotive -40°C to +125°C temperature range.

Applications

Automotive Interior: Map, Courtesy, and Cluster Lighting

Automotive Exterior: Tail Lights and CHMSL

Warning Lights for Emergency Vehicles

Navigation and Marine Indicators

General Illumination

Signage, Gasoline Canopies, Beacons

Features

- ♦ +6.5V to +40V Operating Range
- ◆ Adjustable Level (35mA to 350mA)
- ♦ ±3.5% Output Current Accuracy
- ♦ Integrated Pass Element with Low-Dropout Voltage (0.5V typ)
- ♦ Output Short-Circuit Protection
- +5V Regulated Output with 4mA Source Capability
- ♦ Thermal Shutdown
- Differential LED Current Sense
- ♦ High-Voltage Enable Pin for Dimming Interface
- ♦ Low Shutdown Supply Current (12µA typ)
- ♦ Low 204mV Current-Sense Reference Reduces Power Losses
- Wave-Shaped Soft Edges Reduce Radiated EMI **During PWM Dimming**
- Available in Small, Thermally Enhanced, 5mm x 5mm, 16-Pin TQFN Package
- ♦ -40°C to +125°C Operating Temperature Range

Ordering Information

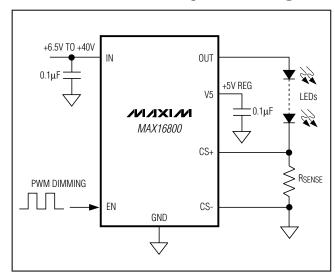
PART	TEMP RANGE	PIN- PACKAGE	PKG CODE		
MAX16800ATE	-40°C to +125°C	16 TQFN-EP*	T1655-3		
MAX16800ATE+	-40°C to +125°C	16 TQFN-EP*	T1655-3		

⁺Denotes lead-free package.

Pin Configuration

TOP VIEW S S S S 9 12 10 N.C. N.C. 13 8 7 N.C. N.C. 14 MIXIM MAX16800 6 15 N.C. ΕN 5 OUT 16 N.C. 3 S. **TQFN**

Simplified Diagram



NIXIN

Maxim Integrated Products 1

^{*}EP = Exposed paddle.

ABSOLUTE MAXIMUM RATINGS

IN, OUT, and EN to GND	0.3V to +45V
CS+, V5 to GND	0.3V to +6V
CS- to GND	
OUT Short Circuited to GND Duration	
(at V _{IN} = +16V)	60 minutes
Maximum Current Into Any Pin (except VIN	and OUT)±20mA

Continuous Power Dissipation (T _A = +70°C) 16-Pin TQFN 5mm x 5mm	
(derate 33.3mW/°C above +70°C)	2666.7mW
Operating Junction Temperature Range	40°C to +125°C
Junction Temperature	+150°C
Storage Temperature Range	
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN}=+12V, EN=V_{IN}, C_{V5}=0.1\mu F$ to GND, $I_{V5}=0$, CS- = GND, connect R_{SENSE} = 0.58Ω between CS+ and CS-. Connect OUT to CS+ (Note 1). T_J = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Supply Voltage Range	V _{IN}	(Note 1)	6.5		40.0	V	
Ground Current	IG	$I_{LOAD} = 350 mA$		1.4	3	mA	
Shutdown Supply Current	ISHDN	EN ≤ 0.6V		12	40	μΑ	
Guaranteed Output Current	lout	$R_{SENSE} = 0.55\Omega$	350			mA	
Output Current Accuracy		35mA < I _{OUT} < 350mA		±3.5		%	
Drangut Voltage (Note 2)	ΔVDO	I _{OUT} = 350mA (current pulsed), 12V < V _{IN} < 40V		0.5	1.2	V	
Dropout Voltage (Note 3)	Δνυσ	I _{OUT} = 350mA (current pulsed), 6.5V < V _{IN} < 40V		0.5	1.5	V	
Output Current Clay Bata		Current rising		5.4		m A /u.o	
Output Current Slew Rate		Current falling		90		mA/µs	
Short-Circuit Current		$V_{OUT} = 0V$, $V_{IN} = 12V$	400	600	800	mA	
LOGIC INPUT							
EN Input Current	I _{EN}				100	nA	
EN Input Voltage High	VIH				2.8	V	
EN Input Voltage Low	V _{IL}		0.6			V	
Turn-On Time	ton	EN rising edge to 90% of OUT		135	350	μs	
CURRENT SENSE (Note 4)							
Regulated RSENSE Voltage	VRSNS	V _{SENSE} = V _{CS+} - V _{CS-}	197	204	211	mV	
Input Current (CS+)		$V_{CS+} = 220 \text{mV}$	-14			μΑ	
Input Current (CS-)		$V_{CS+} = 220 \text{mV}$			+84	μA	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{IN} = +12V, EN = V_{IN}, C_{V5} = 0.1\mu\text{F to GND}, I_{V5} = 0$, CS- = GND, connect R_{SENSE} = 0.58Ω between CS+ and CS-. Connect OUT to CS+ (Note 1). T_J = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
THERMAL OVERLOAD						
Thermal-Shutdown Temperature				+155		°C
Thermal-Shutdown Hysteresis				23		°C
+5V REGULATOR						
Output Voltage Regulation	V5		5.0	5.27	5.5	V
Output Voltage Change	ΔV5	0 < I _{LOAD} < 4mA (Note 5)		15	60	Ω
V5 Short-Circuit Current		V5 = 0V (Note 6)		30		mA

Note 1: Resistors were added from OUT to CS+ to aid with the power dissipation during testing.

Note 2: All devices 100% production tested at TA = +25°C. Limits over the operating temperature range are guaranteed by design.

Note 3: Dropout is measured as follows:

Connect R_O = 27Ω from OUT to CS+. Connect R_{SENSE} = 0.58Ω from CS+ to CS-. Set V_{IN} = +12V (record V_{OUT} as V_{OUT1}). Reduce V_{IN} until V_{OUT} = $0.97 \times V_{OUT1}$ (record as V_{IN2} and V_{OUT2}). $\Delta V_{DO} = V_{IN2} - V_{OUT2}$.

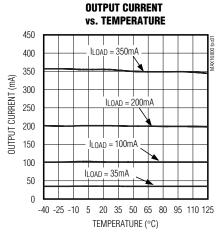
Note 4: $I_{V5} = 0mA$.

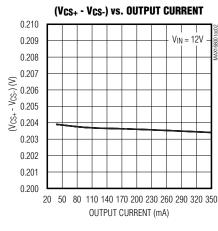
Note 5: Current regulation varies with V5 load (see the Typical Operating Characteristics).

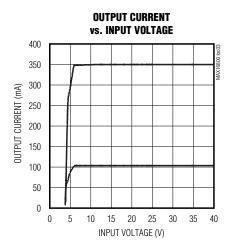
Note 6: Thermal shutdown does not function if the output of the 5V reference is shorted to ground.

_Typical Operating Characteristics

 $(V_{IN} = V_{EN} = +12V, T_A = +25$ °C, unless otherwise noted.)

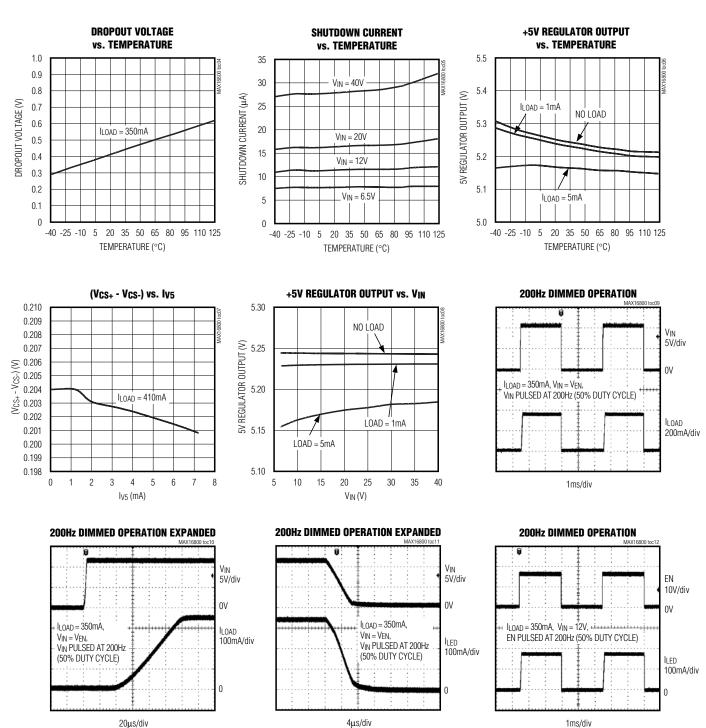






Typical Operating Characteristics (continued)

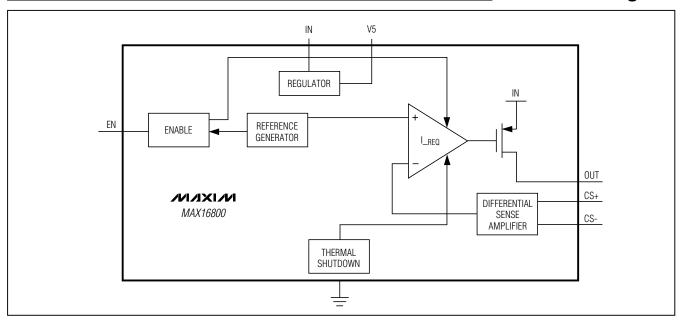
 $(V_{IN} = V_{EN} = +12V, T_A = +25^{\circ}C, unless otherwise noted.)$



Pin Description

PIN	NAME	FUNCTION
1, 16	OUT	Current-Regulated Output. Connect pin 1 to pin 16.
2, 3	IN	Positive Input Supply. Bypass IN with a 0.1µF (min) capacitor to GND. Connect pin 2 to pin 3.
4–8, 13, 14	N.C.	Not Internally Connected
9	CS+	Positive Input of the Internal Differential Amplifier. Connect the current-sense resistor between CS+ and CS- to program the output current level.
10	CS-	Negative Input of the Internal Differential Amplifier. Connect the current-sense resistor between CS- and CS+ to program the output current level.
11	V5	+5V Regulated Output. Connect a 0.1µF capacitor from V5 to GND.
12	GND	Ground
15	EN	Enable Input. Drive EN high to enable the output.
_	EP	Exposed Pad. Connect to the ground plane for effective power dissipation. Do not use as the only ground connection.

Functional Diagram



Detailed Description

The MAX16800 is a high-current regulator capable of providing up to a total of 350mA of current to one or more strings of high-brightness LEDs. A wide operating input voltage range of +6.5V to +40V makes the MAX16800 ideal for automotive applications. A +5V regulated output provides up to 4mA of current to power external circuitry. In addition, the MAX16800 fea-

tures thermal and output short-circuit protection. The wide operating voltage range helps protect the MAX16800 against large transients such as those found in load-dump situations up to 40V.

The MAX16800 uses a feedback loop to control the output current. The differential voltage across the sense resistor is compared to a fixed reference voltage, and the error is amplified to serve as the drive to the internal

power series pass device (see the *Functional Diagram*). The regulation point is factory set at (V_{CS+} - V_{CS-}) = 204mV ±3.5%. The regulated current is user defined by the value of R_{SENSE}.

The MAX16800 is a current controller internally optimized for driving the impedance range expected from one or more HB LEDs.

+5V Regulator

The MAX16800 includes a fixed +5V output regulator that delivers up to 4mA of load current for low-power applications throughout the +6.5V to +40V input voltage range. Connect a 0.1µF compensation capacitor from V5 to ground. Shorting V5 to ground disables the thermal shutdown.

Thermal Protection

The MAX16800 enters a thermal-shutdown mode in the event of overheating. This typically occurs in overload or output short-circuit conditions. When the junction temperature exceeds $T_J = +155^{\circ}\text{C}$ (typ), the internal thermal-protection circuitry turns off the series pass device. The MAX16800 recovers from thermal-shutdown mode once the junction temperature drops by 23°C (typ). The part will therefore protect itself by thermally cycling in the event of a short-circuit or overload condition.

Applications Information

Programming the LED Current

The MAX16800 uses a sense resistor across CS+ and CS- to set the LED current. The differential sense ampli-

fier connected across RSENSE provides ground-loop immunity and low-frequency noise rejection. The LED current is given by the equation below:

ILED = VSENSE / RSENSE

Input Voltage Considerations

For proper operation, the minimum input voltage must always be +1.2V (+1.5V for V_{IN} <+12V) higher than the worst-case sum of all the forward drops of all seriesconnected LEDs to the output of the MAX16800. The minimum operating voltage of the device is +6.5V. The device will operate below +6.5V; however, output current may not meet the full regulation specification (see the *Typical Operating Characteristics*).

Low-Frequency PWM at the Output

The MAX16800 provides pulsed or chopped current dimming. Generally, high-brightness LEDs are binned to match at their full-rated current; however, LEDs from the same bin exhibit poor matching at currents other than full-rated current. To achieve uniformity, HB LED manufacturers recommend PWM pulsing of the LED current at their full-rated value. There are two methods for producing a PWM output. One method is by pulsing the enable input (EN) while having a constant voltage at IN. The other method is to connect EN to IN and pulse both EN and IN. Both methods generate a regulated-amplitude PWM current (variable duty cycle) that can provide control over the LED brightness (see Figures 1 and 2).

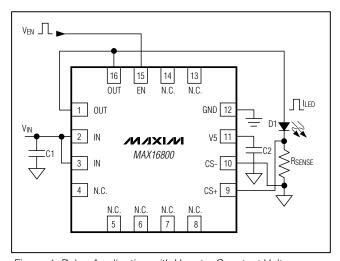


Figure 1. Pulse Application with V_{IN} at a Constant Voltage

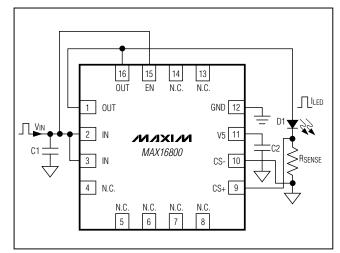
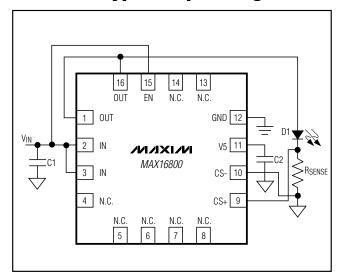


Figure 2. Pulse Application with EN Connected to VIN

Typical Operating Circuit

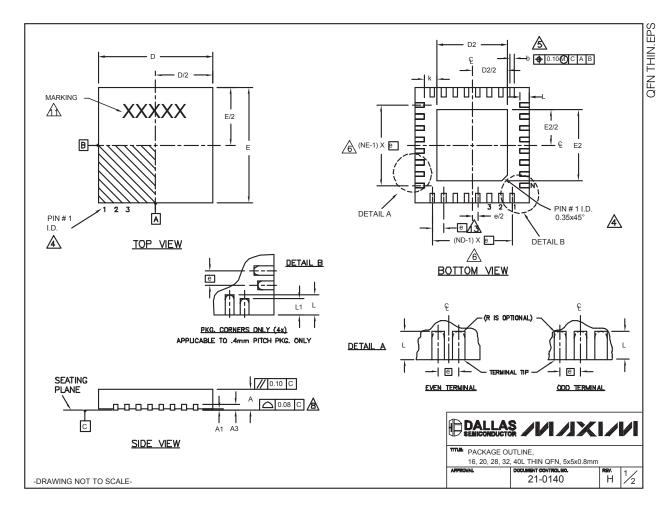
_Chip Information

PROCESS: BiCMOS



Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



Package Information (continued)

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	COMMON DIMENSIONS															
PKG.	16L 5x5 20L 5x5			5	28L 5x5			32L 5x5			40L 5x5					
SYMBOL	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
А	0.70	0.75	0.80	0.70	0.75	0.80	0.70	0.75	0.80	0.70	0.75	0.80	0.70	0.75	0.80	
A1	0	0.02	0.05	0	0.02	0.05	0	0.02	0.05	0	0.02	0.05	0	0.02	0.05	
A3	0.	20 RE	F.	0.	20 RE	F.	0.	0.20 REF.		0.	0.20 REF.			0.20 REF.		
b	0.25	0.30	0.35	0.25	0.30	0.35	0.20	0.25	0.30	0.20	0.25	0.30	0.15	0.20	0.25	
D	4.90	5.00	5.10	4.90	5.00	5.10	4.90	5.00	5.10	4.90	5.00	5.10	4.90	5.00	5.10	
E	4.90	5.00	5.10	4.90	5.00	5.10	4.90	5.00	5.10	4.90	5.00	5.10	4.90	5.00	5.10	
е	0	.80 B	SC.	0.65 BSC.		SC.	0.50 BSC.		0	.50 BS	SC.	0.40 BSC.		SC.		
k	0.25	-	-	0.25	-	-	0.25	-	-	0.25	-	-	0.25	0.35	0.45	
L	0.30	0.40	0.50	0.45	0.55	0.65	0.45	0.55	0.65	0.30	0.40	0.50	0.40	0.50	0.60	
L1	-	-	-	-	-	-	-	-	-	-	-	-	0.30	0.40	0.50	
N		16		20		28		32		40						
ND		4		5		7		8		10						
NE		4		5		7		8			10					
JEDEC		WHHE	3		WHHO		V	VHHD	1-1	WHHD-2						

NΩ	TF	S٠

- 1. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994
- 2. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.
- 3. N IS THE TOTAL NUMBER OF TERMINALS.

THE TERMINAL #1 IDENTIFIER AND TERMINAL NUMBERING CONVENTION SHALL CONFORM TO JESD 95-1 SPP-012. DETAILS OF TERMINAL #1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE TERMINAL #1 IDENTIFIER MAY BE EITHER A MOLD OR MARKED FEATURE.

DIMENSION 6 APPLIES TO METALLIZED TERMINAL AND IS MEASURED BETWEEN 0.25 mm AND 0.30 mm FROM TERMINAL TIP.

ND AND NE REFER TO THE NUMBER OF TERMINALS ON EACH D AND E SIDE RESPECTIVELY.

DEPOPULATION IS POSSIBLE IN A SYMMETRICAL FASHION.

▲ COPLANARITY APPLIES TO THE EXPOSED HEAT SINK SLUG AS WELL AS THE TERMINALS.

 DRAWING CONFORMS TO JEDEC MO220, EXCEPT EXPOSED PAD DIMENSION FOR T2855-1, T2855-3, AND T2855-6.

WARPAGE SHALL NOT EXCEED 0.10 mm.

11. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

12. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.

LEAD CENTERLINES TO BE AT TRUE POSITION AS DEFINED BY BASIC DIMENSION "e", ±0.05.

-DRAWING NOT TO SCALE-

EXPOSED PAD VARIATIONS								
PKG.	D2				E2		L	DOWN BONDS
CODES	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	±0.15	ALLOWED
T1655-1	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T1655-2	3.00	3.10	3.20	3.00	3.10	3.20	**	YES
T1655N-1	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T2055-2	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T2055-3	3.00	3.10	3.20	3.00	3.10	3.20	**	YES
T2055-4	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T2055-5	3.15	3.25	3.35	3.15	3.25	3.35	0.40	YES
T2855-1	3.15	3.25	3.35	3.15	3.25	3.35	**	NO
T2855-2	2.60	2.70	2.80	2.60	2.70	2.80	**	NO
T2855-3	3.15	3.25	3.35	3.15	3.25	3.35	**	YES
T2855-4	2.60	2.70	2.80	2.60	2.70	2.80	**	YES
T2855-5	2.60	2.70	2.80	2.60	2.70	2.80	**	NO
T2855-6	3.15	3.25	3.35	3.15	3.25	3.35	**	NO
T2855-7	2.60	2.70	2.80	2.60	2.70	2.80	**	YES
T2855-8	3.15	3.25	3.35	3.15	3.25	3.35	0.40	YES
T2855N-1	3.15	3.25	3.35	3.15	3.25	3.35	**	NO
T3255-2	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T3255-3	3.00	3.10	3.20	3.00	3.10	3.20	**	YES
T3255-4	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T3255N-1	3.00	3.10	3.20	3.00	3.10	3.20	**	NO
T4055-1	3.20	3.30	3.40	3.20	3.30	3.40	**	YES

** SEE COMMON DIMENSIONS TABLE



PACKAGE OUTLINE,

16, 20, 28, 32, 40L THIN QFN, 5x5x0.8mm

PPROVAL DOCUMENT CONTROL NO. 21-0140

H 2/2

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