Dual EIA-423/EIA-232D Line Driver

The MC3488A dual is single–ended line driver has been designed to satisfy the requirements of EIA standards EIA–423 and EIA–232D, as well as CCITT X.26, X.28 and Federal Standard FIDS1030. It is suitable for use where signal wave shaping is desired and the output load resistance is greater than 450 ohms. Output slew rates are adjustable from 1.0 μ s to 100 μ s by a single external resistor. Output level and slew rate are insensitive to power supply variations. Input undershoot diodes limit transients below ground and output current limiting is provided in both output states.

The MC3488A has a standard 1.5 V input logic threshold for TTL or NMOS compatibility.

- PNP Buffered Inputs to Minimize Input Loading
- Short Circuit Protection
- Adjustable Slew Rate Limiting
- MC3488A Equivalent to 9636A
- Output Levels and Slew Rates are Insensitive to Power Supply Voltages
- No External Blocking Diode Required for V_{EE} Supply
- Second Source µA9636A



DUAL EIA-423/EIA-232D DRIVER

> SEMICONDUCTOR TECHNICAL DATA

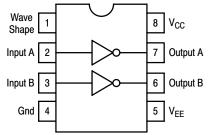


D SUFFIX PLASTIC PACKAGE CASE 751 (SO-8)



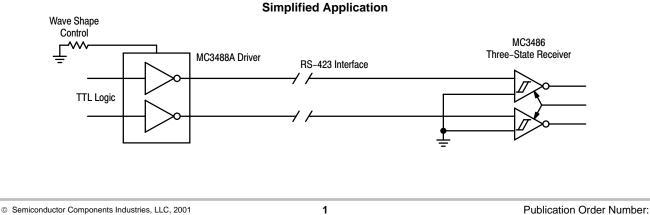
MC3488A/D





ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC3488AP1	T _A = 0 to +70°C	Plastic DIP
MC3488AD	$T_{A} = 0.00 + 70^{-1}C$	SO–8



August, 2001 – Rev. 3

This datasheet has been downloaded from http://www.digchip.com at this page

MAXIMUM RATINGS (Note 1)

Rating	Symbol	Value	Unit
Power Supply Voltages	V _{CC} V _{EE}	+ 15 – 15	V
Output Current Source Sink	I _{O +} I _{O -}	+ 150 - 150	mA
Operating Ambient Temperature	T _A	0 to + 70	°C
Junction Temperature Range	TJ	150	°C
Storage Temperature Range	T _{stg}	– 65 to + 150	°C

RECOMMENDED OPERATING CONDITIONS

Characteristic	Symbol	Min	Тур	Max	Unit
Power Supply Voltages	V _{CC} V _{EE}	10.8 - 13.2	12 - 12	13.2 - 10.8	V
Operating Temperature Range	T _A	0	25	70	°C
Wave Shaping Resistor	R _{WS}	10	-	1000	kΩ

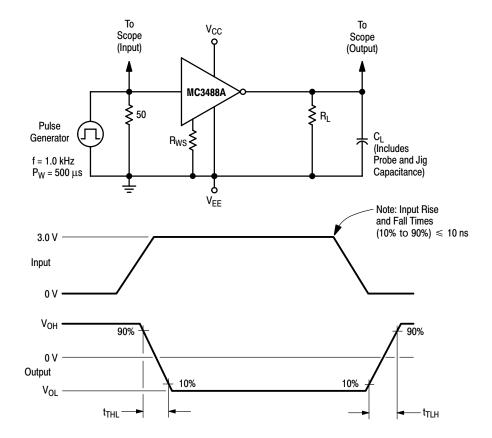
TARGET ELECTRICAL CHARACTERISTICS (Unless otherwise noted, specifications apply over recommended operating conditions)

Characteristic	Symbol	Min	Тур	Max	Unit
Input Voltage – Low Logic State	V _{IL}	-	-	0.8	V
Input Voltage – High Logic State	V _{IH}	2.0	-	-	V
Input Current – Low Logic State (V _{IL} = 0.4 V)	IL	- 80	-	-	μΑ
Input Current – High Logic State $(V_{IH} = 2.4 V)$ $(V_{IH} = 5.5 V)$	l _{IH1} l _{IH2}			10 100	μΑ
Input Clamp Diode Voltage (I _{IK} = - 15 mA)	V _{IK}	- 1.5	-	-	V
$\begin{array}{llllllllllllllllllllllllllllllllllll$	V _{OL}	- 6.0 - 6.0 - 6.0	_ _ _	- 5.0 - 5.0 - 4.0	V
$\begin{array}{ll} & \text{Output Voltage}-\text{High Logic State} \\ & (R_{L}=\infty) & \text{EIA-423} \\ & (R_{L}=3.0 \text{ k}\Omega) & \text{EIA-232D} \\ & (R_{L}=450 \ \Omega) & \text{EIA-423} \end{array}$	V _{OH}	5.0 5.0 4.0	_ _ _	6.0 6.0 6.0	V
Output Resistance ($R_L \ge 450 \Omega$)	R _O	-	25	50	Ω
$ Output Short-Circuit Current (Note 2) \\ (V_{in} = V_{out} = 0 V) \\ (V_{in} = V_{IH(Min)}, V_{out} = 0 V) $	I _{OSH} I _{OSL}	– 150 + 15		- 15 + 150	mA
Output Leakage Current (Note 3) (V _{CC} = V _{EE} = 0 V, $-6.0 V \le V_0 \le 6.0 V$)	I _{ox}	- 100	-	100	μΑ
Power Supply Currents $(R_W = 100 \text{ k}\Omega, R_L = \infty, V_{IL} \leq V_{in} \leq V_{IH})$	I _{CC} I _{EE}	_ _ 18		+ 18 -	mA

NOTES: 1. Devices should not be operated at these values. The "Electrical Characteristics" provide conditions for actual device operation. 2. One output shorted at a time. 3. No V_{EE} diode required.

Characteristic	Symbol	Min	Тур	Max	Unit
Transition Time, Low–to–High State Output	t _{TLH}				μs
$(R_W = 10 \text{ k}\Omega)$		0.8	-	1.4	
$(R_W = 100 \text{ k}\Omega)$		8.0	-	14	
$(R_W = 500 \text{ k}\Omega)$		40	-	70	
$(R_W = 1000 \text{ k}\Omega)$		80	_	140	
ransition Time, High-to-Low State Output	t _{THL}				μs
$(R_W = 10 \text{ k}\Omega)$		0.8	-	1.4	
$(R_W = 100 \text{ k}\Omega)$		8.0	_	14	
$(R_W = 500 \text{ k}\Omega)$		40	_	70	
$(R_W = 1000 \text{ k}\Omega)$		80	_	140	

TRANSITION TIMES (Unless otherwise noted, C_L = 30 pF, f = 1.0 kHz, V_{CC} = – V_{EE} = 12.0 V ± 10%, T_A = 25°C, R_L = 450 Ω . Transition times measured 10% to 90% and 90% to 10%)





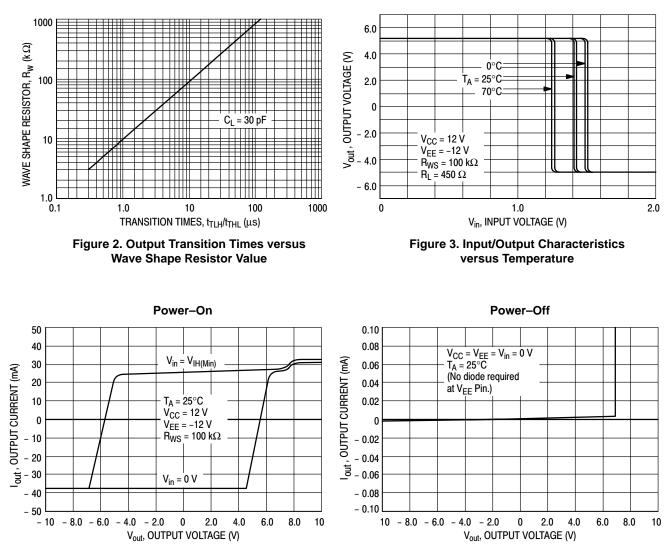
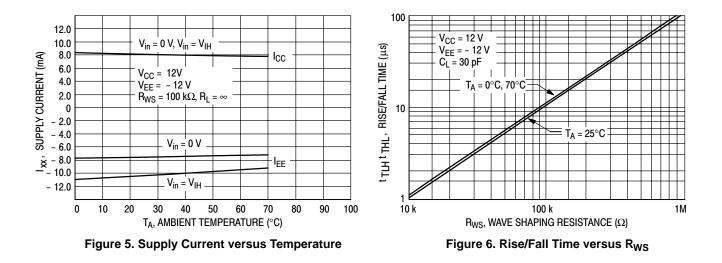
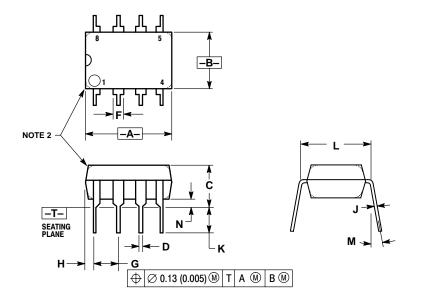


Figure 4. Output Current versus Output Voltage



PACKAGE DIMENSIONS

P1 SUFFIX PLASTIC PACKAGE CASE 626-05 ISSUE L





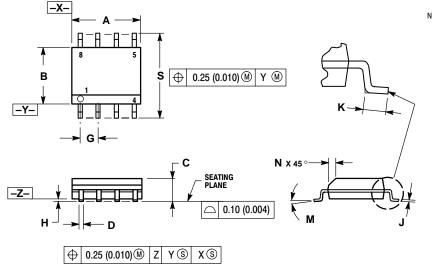
NOTES: 1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL. 2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS). 3. DIMENSIONING AND TOLERANCING PER ANSI V14 64 1092

1	(14.5N	l, 1982.		
		MILLIN	IETERS	IN

	MILLIN	IETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	9.40	10.16	0.370	0.400
В	6.10	6.60	0.240	0.260
С	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54	BSC	0.100 BSC	
Η	0.76	1.27	0.030	0.050
L	0.20	0.30	0.008	0.012
Κ	2.92	3.43	0.115	0.135
Г	7.62 BSC		0.300	BSC
Μ		10°		10°
Ν	0.76	1.01	0.030	0.040

PACKAGE DIMENSIONS

D SUFFIX PLASTIC PACKAGE CASE 751-07 **ISSUE W**



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER. 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION. 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE. 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27	7 BSC	0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
Μ	0 °	8 °	0 °	8 °
Ν	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

<u>Notes</u>

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