

# **RJK03E3DNS**

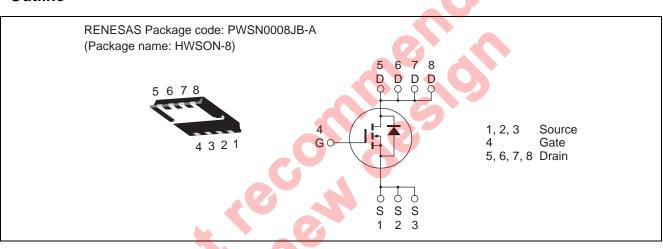
Silicon N Channel Power MOS FET **Power Switching** 

R07DS0659EJ0300 (Previous: REJ03G1905-0200) Rev.3.00 Feb 01, 2012

#### Features

- High speed switching
- Capable of 4.5 V gate drive •
- Low drive current
- High density mounting
- Low on-resistance
- $R_{DS(on)} = 9.0 \text{ m}\Omega \text{ typ.}$  (at  $V_{GS} = 10 \text{ V}$ )
- Pb-free
- Halogen-free

#### Outline



## Absolute Maximum Ratings

		(Ta = 25°C)		
Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	30	V	
Gate to source voltage	V <sub>GSS</sub>	±20	V	
Drain current	I <sub>D</sub>	14	А	
Drain peak current	Note1 I <sub>D(pulse)</sub>	56	А	
Body-drain diode reverse drain current	I <sub>DR</sub>	14	А	
Avalanche current	I <sub>AP</sub> Note 2	6.5	А	
Avalanche energy	E <sub>AR</sub> Note 2	4.23	mJ	
Channel dissipation	Pch Note3	10	W	
Channel to case thermal impedance	θch-c <sup>Note3</sup>	12.5	°C/W	
Channel temperature	Tch	150	۵°	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Tc = 25°C

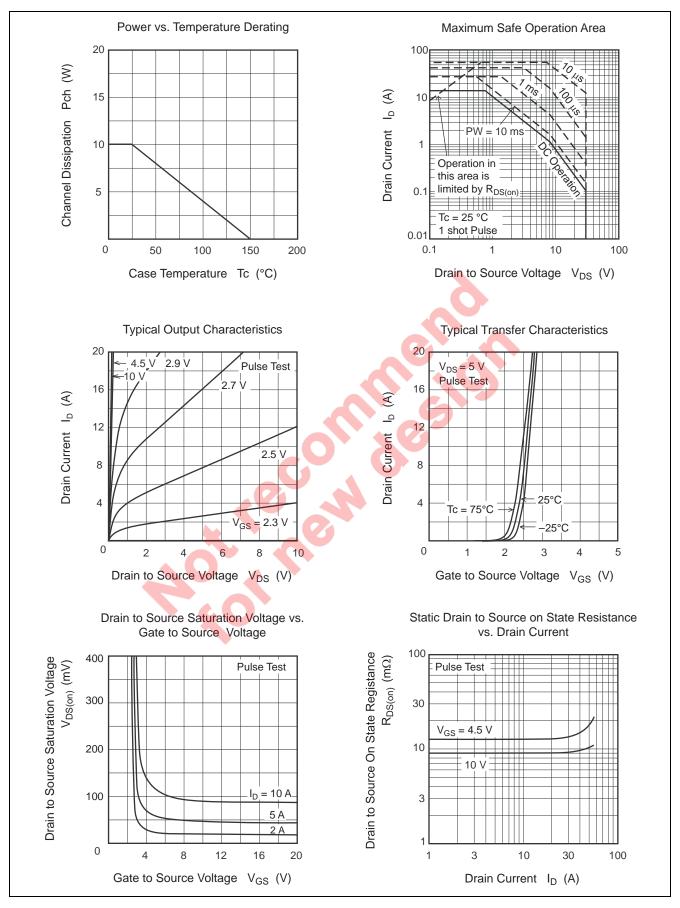


### **Electrical Characteristics**

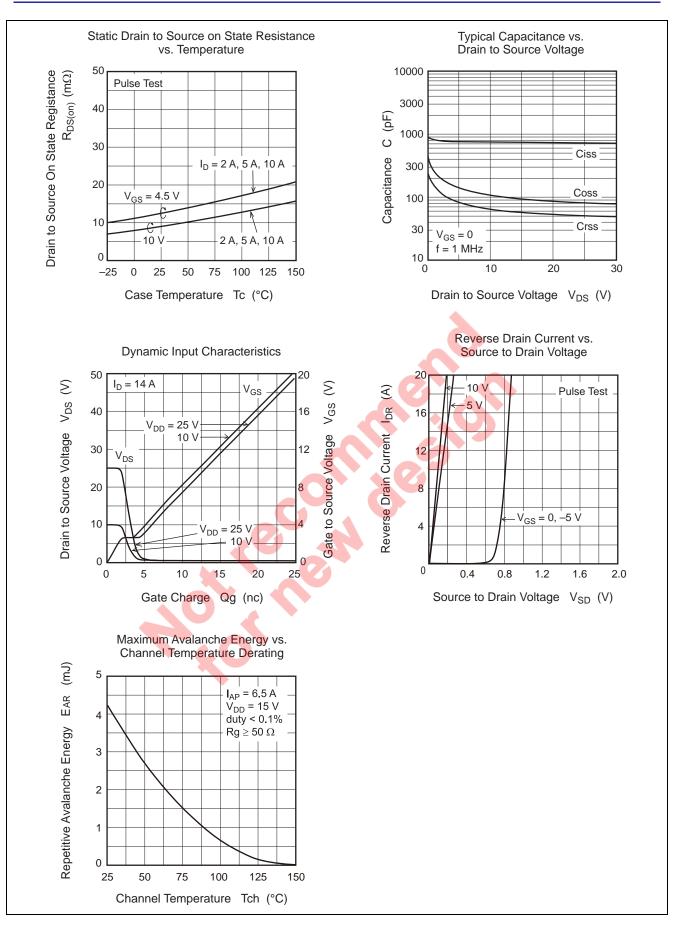
			-			$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	—	±0.1	μA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μA	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	9.0	11.6	mΩ	$I_D = 7 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R <sub>DS(on)</sub>	_	12.6	17.6	mΩ	$I_D = 7 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$
Forward transfer admittance	y <sub>fs</sub>		27	—	S	$I_D = 7 \text{ A}, V_{DS} = 5 \text{ V}^{Note4}$
Input capacitance	Ciss		750	1050	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	—	108	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	63		pF	f = 1 MHz
Gate Resistance	Rg		1.7	3.4	Ω	
Total gate charge	Qg	_	5.7		nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	2.2	_	nC	V <sub>GS</sub> = 4.5 V
Gate to drain charge	Qgd	_	1.6		nC	I <sub>D</sub> = 14 A
Turn-on delay time	t <sub>d(on)</sub>	_	7.1	-	ns	$V_{GS} = 10 \text{ V}, I_D = 7 \text{ A}$
Rise time	tr		3.8	ľ	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t <sub>d(off)</sub>		32		ns	R <sub>L</sub> = 1.43 Ω
Fall time	t <sub>f</sub>	_	4.7		ns	Rg = 4.7 Ω
Body–drain diode forward voltage	V <sub>DF</sub>	_	0.84	1.1	V	$I_F = 14 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body–drain diode reverse recovery	t <sub>rr</sub>	_	11	_	ns	I <sub>F</sub> =14 A, V <sub>GS</sub> = 0
time						di <sub>F</sub> / dt = 100 A/ μs
time Notes: 4. Pulse test			5			

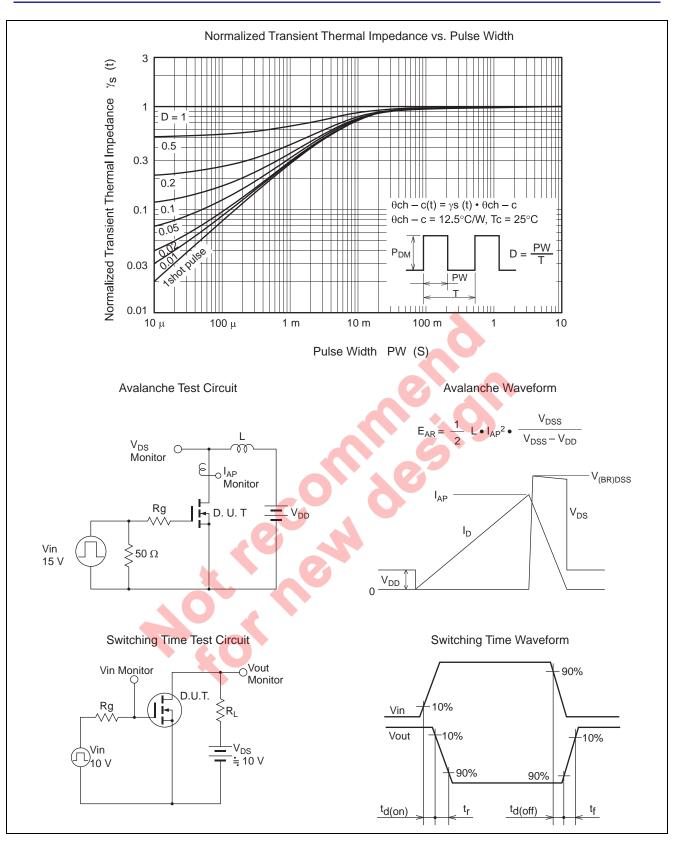


#### **Main Characteristics**



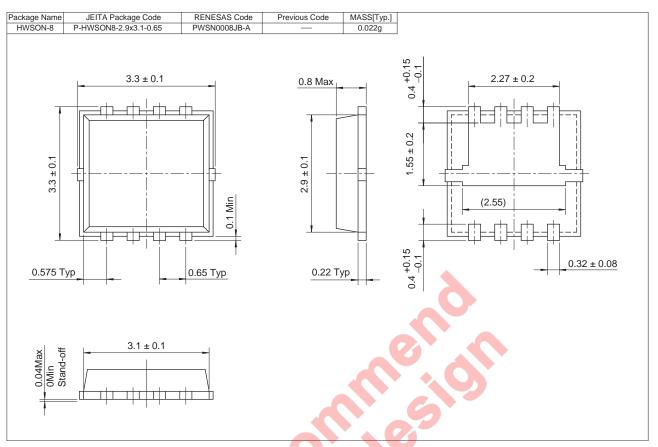








#### **Package Dimensions**



### **Ordering Information**

Orderable Part Number	Quantity		Shipping Container
RJK03E3DNS-00-J5	5000 pcs	Тар	ing

Note: The symbol of 2nd "-" is occasionally presented as "#".



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