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1W, 1206, LF Type Low Resistance Chip Resistor (Lead / Halogen Free)

1. Scope

This specification applies to 1.6mm x 3.2mm size 1W.

2. Type Designation

Where

- (1) Series No.
- (2) 6F = 1W
- (3) Resistance value: Four digits of number

For example:

$$R005 = 5m\Omega$$

$$R010 = 10 \text{m}\Omega$$

(4) Tolerance:

Refer to paragraph 4

(5) NH = Sn plating (Lead free / Halogen free)

3. Dimensions and schematic

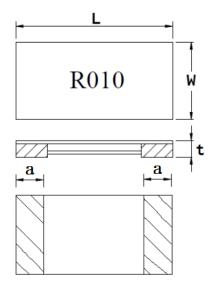


Figure 1. Construction and Dimensions

Code	Dimensions (mm)			
Letter	1632			
L	3.2 ± 0.20			
W	1.6 ± 0.20			
a	$3m\Omega$	0.65 ± 0.15		
	4~20mΩ	0.50 ± 0.15		
t	0.6 ± 0.20			

Note: Marking (No Direction)

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4. Specification

Characteristics	Feature	
Power Rating*	1W	
Resistance Value	$3 \sim 20 \text{ m}\Omega$	
Temperature Coefficient of Resistance	± 100 ppm/°C	
Operation Temperature Range	-55°C ~+170°C	
Resistance Tolerance	$\pm 1\%(F), \pm 2\%(G), \pm 5\%(J)$	
Insulation Resistance	Over $100 \mathrm{M}\Omega$	
Maximum Working Voltage (V)	(P*R) ^{1/2}	

Note *:

Power rating is based on continuous full load operation at rated ambient temperature of 70°C . For resistors operated at ambient temperature in excess of 70°C , the maximum load shall be derated in accordance with the following curve.

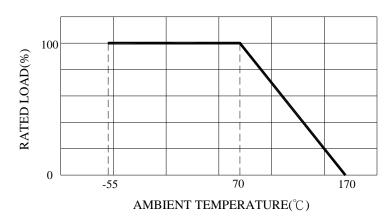


Figure 2. : Power Temperature Derating Curve

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5. Reliability Performance

Test Item	Condition of Test	Requirements	
Short Time Overload	2.5 * Rated power for 5 seconds	$\Delta R: \pm 1.0\%$	
	Refer to JIS C 5201-1 4.13		
Thermal Shock	-55 ~125°C 100 cycles, 15 min at each extreme	$\Delta R: \pm 1.0\%$	
	condition		
	Refer to JIS C 5201-1 4.19		
Low Temperature Storage	Kept at -55°C, 1,000 hours $\Delta R : \pm 0.5\%$		
	Refer to JIS C 5201-1 4.23.4		
High Temperature Exposure	Kept at 170°C for 1,000 hours	$\Delta R: \pm 1.0\%$	
	Refer to JIS C 5201-1 4.23.2		
Solderability	Temperature of Solder : $245 \pm 5^{\circ}$ C	Uniform coating of solder	
	Immersion Duration : 2 ± 0.5 second	cover minimum of 95%	
	Refer to JIS C 5201-1 4.17	surface being immersed	
Resistance to Soldering Heat	Dipped into solder at $270 \pm 5^{\circ}$ C for 10	$\Delta R:\pm 0.5\%$	
	± 1 seconds		
	Refer to JIS C 5201-1 4.18		
Load Life	Rated voltage for 1.5 hours followed by a pause	$\Delta R: \pm 1.0\%$	
	0.5 hour at $70 \pm 2^{\circ}$ C.		
	Cycle repeated 1000 hours		
	Refer to JIS C 5201-1 4.25		
Damp Heat with Load	$40 \pm 2^{\circ}$ C with relative humidity 90% to	$\Delta R: \pm 1.0\%$	
	95%. D.C. rated voltage for 1.5 hours ON		
	and 30 minutes OFF. Cycle repeated 1,000		
	hours		
	Refer to JIS C 5201-1 4.24		
Bending Test	Glass-Epoxy board thickness: 1.6mm	$\Delta R: \pm 0.5\%$	
	Bending width: 2mm		
	Between the fulcrums: 90mm		
	Refer to JIS C 5201-1 4.33		

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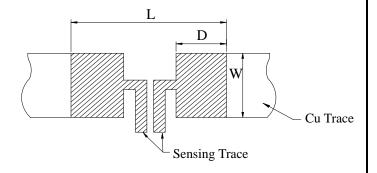
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6. Recommended Solder Pad Dimensions

1632	W (mm)	L (mm)	D (mm)	t (μm)
	1.78	4.14	1.37	105

t: Copper foil minimum thickness of PCB



Note: We recommend there is no circuit design between pads to avoid circuit short.

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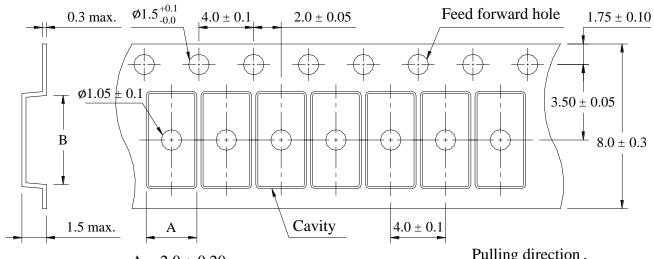
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7. Packaging

7-1 Dimensions

7-1-1 Tape packaging dimensions

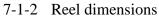


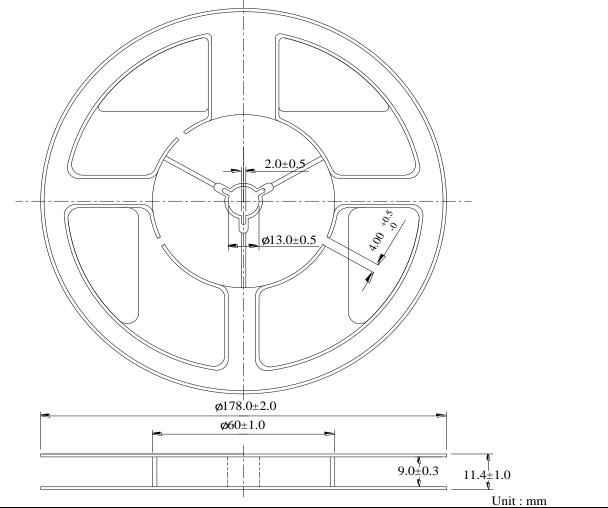
 $A = 2.0 \pm 0.20$

 $B = 3.6 \pm 0.20$

Pulling direction

Unit: mm





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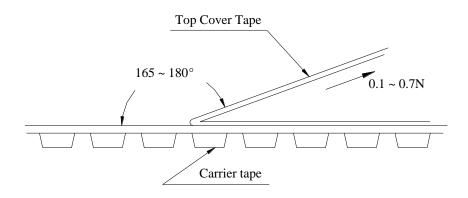
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7-2 Peel Strength of Top Cover Tape

The peel speed shall be about 300mm/min.

The peel force of top cover tape shall between 0.1 to 0.7N



7-3 Number of Taping

4,000 pieces / reel

7-4 Label marking

The following items shall be marked on the reel.

- (1) Type designation
- (2) Quantity
- (3) Manufacturing date code
- (4) Manufacturer's name
- (5) The country of origin