

Voltage Controlled Clock OSC.

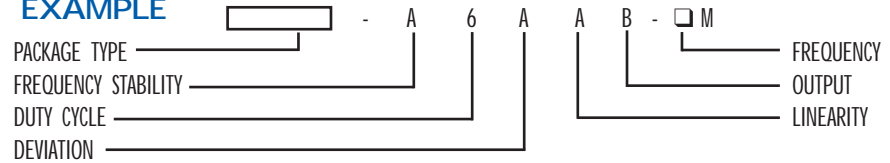
Surface Mount & Thru Hole

ADVCXO Series



Package Type	ADVCXO G4 SMD ADVCXO G6 SMD	pg31 pg31	ADVCXO I SMD ADVCXO H Thru Hole	pg32 pg33
Frequency Stability	±100 ppm ±50 ppm ±30 ppm	A B C	±25 ppm Custom	D E
Duty Cycle	45/55%	5	40/60%	6
Deviation	±100 ppm min ±75ppm min	A B	±50 ppm min ±200 ppm min	C D
Linearity	20% 15%	A B	10% None	C Blank
Output	TTL CMOS	A B	CMOS Compatible	C

EXAMPLE

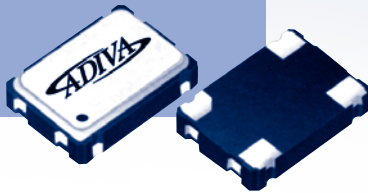


Voltage Controlled Clock OSC.

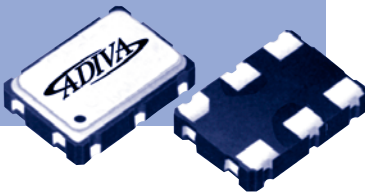
Surface Mount

ADVCXOG Series (G4, G6)

ADVCXOG4



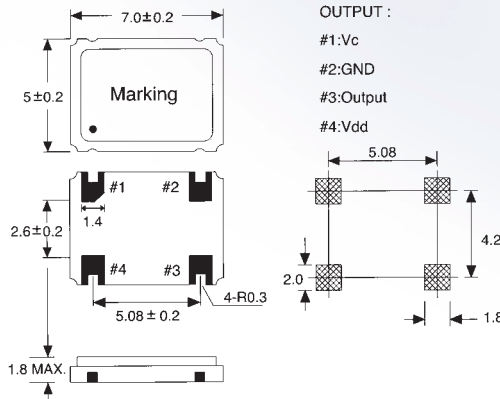
ADVCXOG6



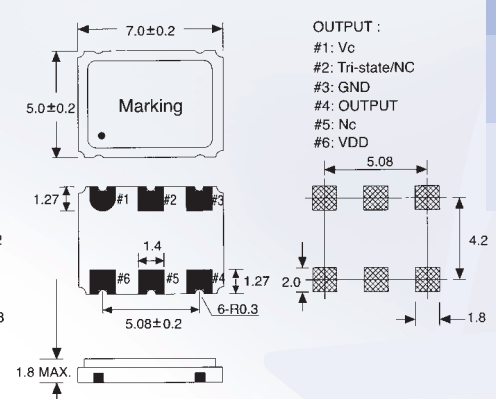
FEATURES

- Seam welding
- Superior quality
- Low power consumption
- Custom requirement available
- Application: Set top box, ADSL modem.

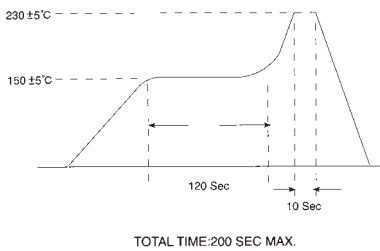
ADVCXOG4 (unit: mm)



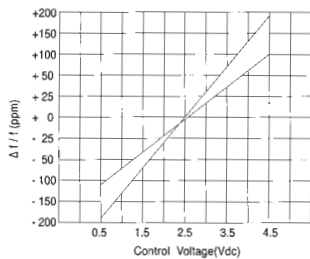
ADVCXOG6 (unit: mm)



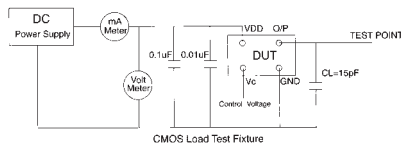
SOLDERING REFLOW PROFILE



FREQUENCY/MODULATION VOLTAGE CHARACTERISTICS

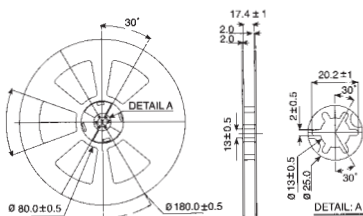


TEST CIRCUIT



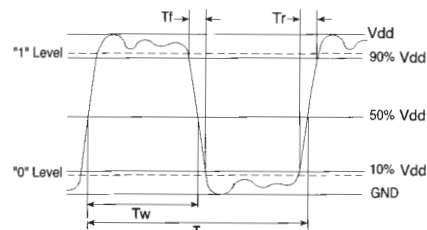
*A 0.1µF tantalum capacitor and 0.01µF chip ceramic capacitor should be connected on the VDD terminal is recommended for the application.

REEL PACKING (unit: mm)



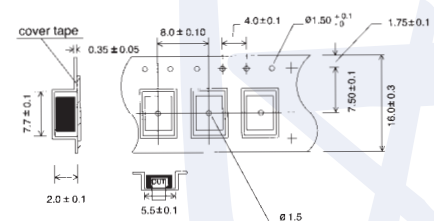
Tape reel 1000pcs/reel

OUTPUT WAVEFORM FOR CMOS



Symmetry=(Tw/T)*100% measured at 50% Vdd

TAPE SPECIFICATIONS



SEE PAGE 30 FOR PART NUMBERING GUIDE