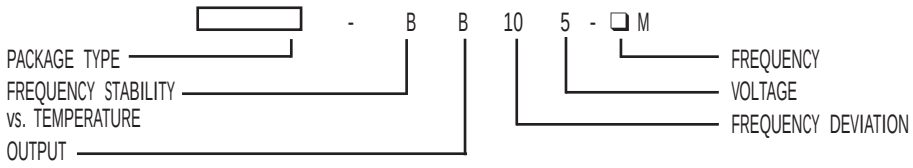
Package Type	ADTCXOH	SMD	pg36	ADVTCXOI	SMD	pg36
	ADTCXOI	SMD	pg36	ADVTCXOJ	SMD	pg36
	ADTCXOJ	SMD	pg36	ADVTCXOK	SMD	pg36
	ADTCXOK	SMD	pg36	ADVTCXOL	SMD	pg36
	ADTCXOL	SMD	pg36	ADTCXOG	Thru Hole	pg37
	ADVTCXOH	SMD	pg36	ADVCTCXOG	Thru Hole	pg38
Frequency Stability vs. Temperature	±5ppm (0°C~50°C)	A		±2.5ppm (-30°C~75°C)	C	
	±2.5ppm (-15°C~55°C)	B		Custom	D	
Output	TTL	A		CMOS Compatible	C	
	CMOS	B		Clipped SINEwave	D	
Frequency Deviation	No Connection	BLANK		±10ppm min	10	
	±5ppm min	5				
Voltage	3.3V	3		5.0V	5	

EXAMPLE



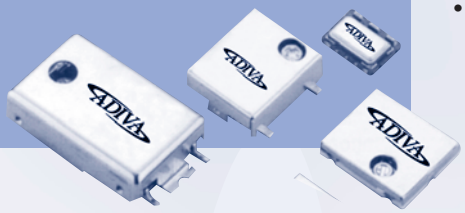
Voltage & Temp. Controlled OSC.

Surface Mount

ADVCTCXO Series (H, I, J, K, L)

ADIVA Technology, Inc.

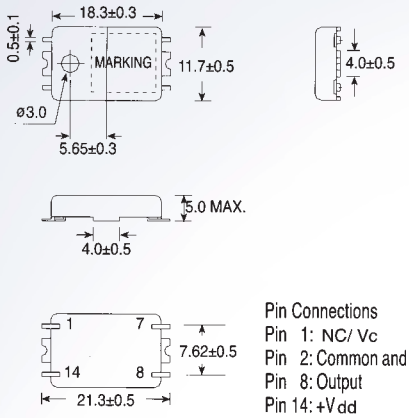
ADVCTCXOH-L



FEATURES

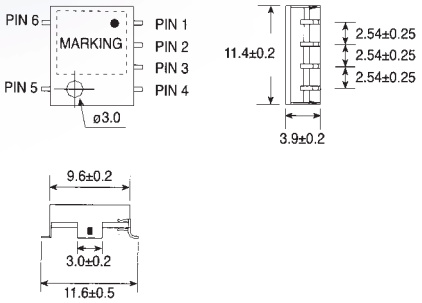
- Ultra miniature package suitable for portable products.
- Excellent frequency stability and aging.
- Custom requirements available.
- Application: Cellular/PCS handsets, Wireless platforms (MMDS, LMDS, Base station satellite communications).

ADVCTCXOH (unit: mm)



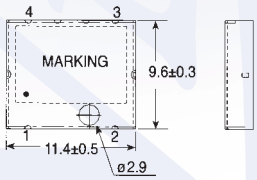
Pin Connections
Pin 1: NC/ Vc
Pin 2: Common and Case
Pin 8: Output
Pin 14: +Vdd

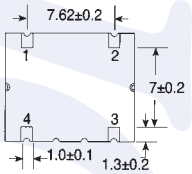
ADVCTCXOI (unit: mm)



Pin Connections
Pin 1: +Vdd
Pin 2: NC/ Vc
Pin 3: GND
Pin 4: Output
Pin 5: Common and Case
Pin 6: Common and Case

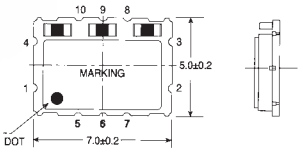
ADVCTCXOJ (unit: mm)

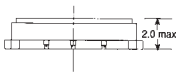




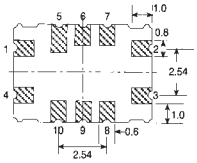
Connections
1: NC/ Vc
2: GND
3: Output
4. + Vdd

ADVCTCXOK (unit: mm)



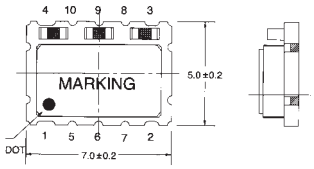


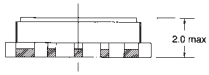
MARKING
4.Part Number
5.Frequency
6.Date Code



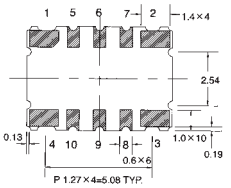
CONNECTIONS
1: NC/ Vc
2: GND
3: Output
4: Vdd
5 ~8 : Internal Programming (keeping floating is recommended)

ADVCTCXOL (unit: mm)





MARKING
1.Frequency
2.Date Code



CONNECTIONS
1: NC/ Vc
2: GND
3: Output
4: Vdd
5 ~10 : Internal Programming (keep floating is recommended)

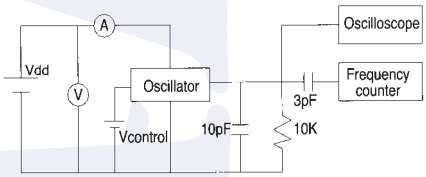
No-clean process is recommended for this part

STANDARD SPECIFICATIONS

Package Type	ADVCXOH, ADVCXOI, ADVCXOJ, ADVCXOK, ADVCXOL	
Standard Frequency	9.6, 10, 12, 12.8, 13, 14.4, 15.36, 19.68, 20 MHz	
Frequency Range	9.6 ~ 35 MHz	9.6 ~ 35 MHz
Frequency Stability vs. Temperature Range	±5 ppm (0ºC ~ 50ºC) ±2.5 ppm (-30ºC ~ 75ºC)	±2.5 ppm (-15ºC ~ 55ºC) Custom
Frequency Stability vs. Voltage	±0.3 ppm (Vdd ±5%)	±0.3 ppm (Vdd ±5%)
Frequency Adjustment	±3 ppm min. (tuned by internal trimmer)	±3 ppm min. (tuned by internal trimmer)
Frequency Control Range	±5 ppm min. ±12 ppm max.	1.5 ±1 Vdc
Supply Voltage	Vdd = 5V	Vdd = 3V
Output Level (Clipped Sinewave)*	1.0 Vpp min.	0.8 Vpp min.
Supply Current	2 mA max.	2 mA max.
Output Load	10K Ω//10 pF	10K Ω//10 pF
Aging	±1 ppm/year	±1 ppm/year

*TTL/CMOS output level available (depended on custom requirement).

TEST CIRCUIT



SEE PAGE 34 FOR PART NUMBERING GUIDE