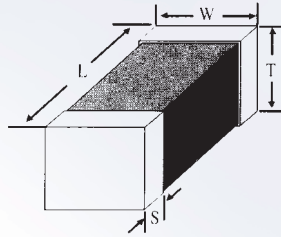


# Multilayer Chip

Surface Mount

ADLL1005 Series

ADLL1005



## SPECIFICATIONS

| SIZE     |      | LENGTH (L)<br>(inch)<br>mm    | WIDTH (W)<br>(inch)<br>mm     | THICKNESS (T)<br>(inch)<br>mm | TERMINAL (S)<br>(inch)<br>mm   |
|----------|------|-------------------------------|-------------------------------|-------------------------------|--------------------------------|
| ADLL1005 | 0402 | (0.040 ± 0.004)<br>1.0 ± 0.10 | (0.020 ± .004)<br>0.50 ± 0.10 | (0.020 ± .004)<br>0.50 ± 0.10 | (0.0092 ± .004)<br>0.23 ± 0.10 |

## STANDARD SPECIFICATIONS

| PACKAGE TYPE       | INDUCTANCE<br>(nH) | Q<br>min. | L/Q FREQ.<br>(MHz) | RDC<br>max. (Ω) | S.R.F.<br>Typ. (MHz) | IDC<br>max. (mA) | QTY/REEL<br>(pcs) |
|--------------------|--------------------|-----------|--------------------|-----------------|----------------------|------------------|-------------------|
| ADLL1005 1N0 □ - T | 1.0 ±0.3           | 8         | 100                | 0.12            | >15000               | 300              | 10,000            |
| ADLL1005 1N2 □ - T | 1.2 ±0.3           | 8         | 100                | 0.12            | >15000               | 300              | 10,000            |
| ADLL1005 1N5 □ - T | 1.5 ±0.3           | 8         | 100                | 0.13            | >15000               | 300              | 10,000            |
| ADLL1005 1N8 □ - T | 1.8 ±0.3           | 8         | 100                | 0.14            | 14000                | 300              | 10,000            |
| ADLL1005 2N2 □ - T | 2.2 ±0.3           | 8         | 100                | 0.16            | 12000                | 300              | 10,000            |
| ADLL1005 2N7 □ - T | 2.7 ±0.3           | 8         | 100                | 0.17            | 9500                 | 300              | 10,000            |
| ADLL1005 3N3 □ - T | 3.3 ±0.3 or ±10%   | 8         | 100                | 0.19            | 8500                 | 300              | 10,000            |
| ADLL1005 3N9 □ - T | 3.9 ±0.3 or ±10%   | 8         | 100                | 0.22            | 7000                 | 300              | 10,000            |
| ADLL1005 4N7 □ - T | 4.7 ±0.3 or ±10%   | 8         | 100                | 0.24            | 6000                 | 300              | 10,000            |
| ADLL1005 5N6 □ - T | 5.6 ±0.3 or ±10%   | 8         | 100                | 0.27            | 5400                 | 300              | 10,000            |
| ADLL1005 6N8 □ - T | 6.8 ±5% or ±10%    | 8         | 100                | 0.32            | 5000                 | 250              | 10,000            |
| ADLL1005 8N2 □ - T | 8.2 ±5% or ±10%    | 8         | 100                | 0.40            | 4600                 | 250              | 10,000            |
| ADLL1005 10N □ - T | 10 ±5% or ±10%     | 8         | 100                | 0.45            | 3700                 | 250              | 10,000            |
| ADLL1005 12N □ - T | 12 ±5% or ±10%     | 8         | 100                | 0.50            | 3200                 | 250              | 10,000            |
| ADLL1005 15N □ - T | 15 ±5% or ±10%     | 8         | 100                | 0.60            | 3100                 | 250              | 10,000            |
| ADLL1005 18N □ - T | 18 ±5% or ±10%     | 8         | 100                | 0.65            | 2900                 | 200              | 10,000            |
| ADLL1005 22N □ - T | 22 ±5% or ±10%     | 8         | 100                | 0.80            | 2100                 | 200              | 10,000            |
| ADLL1005 27N □ - T | 27 ±5% or ±10%     | 8         | 100                | 0.90            | 1900                 | 200              | 10,000            |
| ADLL1005 33N □ - T | 33 ±5% or ±10%     | 8         | 100                | 1.00            | 1600                 | 200              | 10,000            |
| ADLL1005 39N □ - T | 39 ±5% or ±10%     | 8         | 100                | 1.20            | 1400                 | 150              | 10,000            |
| ADLL1005 47N □ - T | 47 ±5% or ±10%     | 8         | 100                | 1.30            | 1200                 | 150              | 10,000            |
| ADLL1005 56N □ - T | 56 ±5% or ±10%     | 8         | 100                | 2.00            | 1100                 | 150              | 10,000            |
| ADLL1005 68N □ - T | 68 ±5% or ±10%     | 8         | 100                | 2.20            | 1000                 | 100              | 10,000            |
| ADLL1005 82N □ - T | 82 ±5% or ±10%     | 8         | 100                | 2.50            | 900                  | 100              | 10,000            |
| ADLL1005 R10 □ - T | 100 ±5% or ±10%    | 8         | 100                | 2.50            | 850                  | 100              | 10,000            |

Operating Temperature Range: -40°C to +100°C.

Storage Temperature Range: -55°C to +100°C.

Testing Method: L and Q: HP 4291B (+16192A), S.R.F. (Self Resonant Frequency): HP 8722D, RDC (DC Resistance): HP 4338B, IDC (Rated Current): HP 4284A.

XTAL

OSC

VCXO  
VCO

TCXO  
VCTCXO

FLTR

RES

IND

INDUCTORS

