

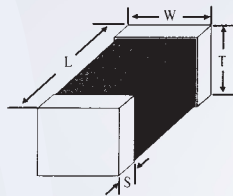
Multilayer Chip

Surface Mount

ADMLIA Series



ADMLIA



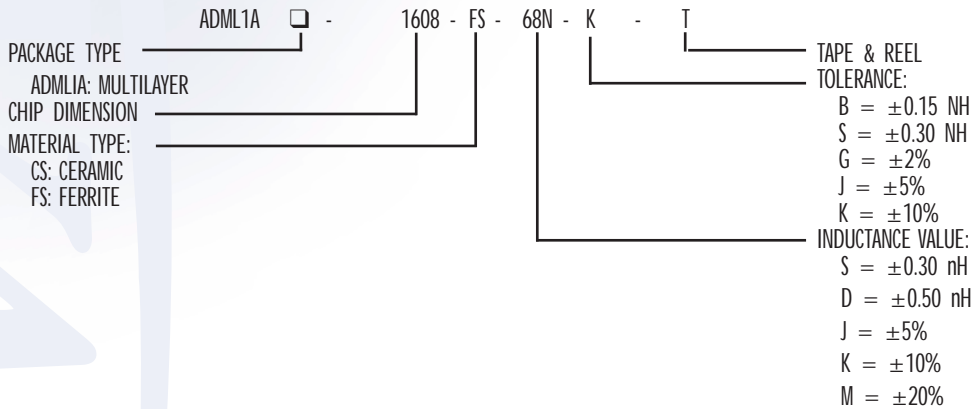
INTRODUCTION

The ADMLIA series are chip inductors widely used in the communication applications such as cellular phones, pagers, computers and other electronic devices. The device features in magnetic shielding which avoids cross coupling and crosstalk.

FEATURES

- Operating Temperature: -40°C to 85°C.
- Excellent solderability and resistance to soldering heat.
- Suitable for flow and reflow soldering.
- Good dimensions, high reliability, and easy surface mount assembly.
- 3 types of materials provide wide range of inductance value for flexible needs.

PART NUMBERING GUIDE



SPECIFICATIONS

SIZE	LENGTH (L) (inch) mm	WIDTH (W) (inch) mm	THICKNESS (T) (inch) mm	TERMINAL (B) (inch) mm
ADMLIA-1005	(0.040 +/- 0.004) 1.0 +/- 0.10	(0.020 +/- 0.004) 0.50 +/- 0.10	(0.020 +/- 0.004) 0.50 +/- 0.10	(0.0092 +/- 0.004) 0.23 +/- 0.10
ADMLIA-1608	(0.063 ± 0.006) 1.60 ± 0.15	(0.031 ± 0.006) 0.80 ± 0.15	(0.031 ± 0.006) 0.80 ± 0.15	(0.016 ± 0.004) 0.30 ± 0.1
ADMLIA-2012	(0.080 ± 0.008) 2.00 ± 0.2	(0.050 ± 0.008) 1.25 ± 0.2	(0.033 ± 0.008) 0.85 ± 0.2	(0.020 ± 0.012) 0.50 ± 0.30
ADMLIA-2012	(0.080 ± 0.008) 2.00 ± 0.2	(0.050 ± 0.008) 1.25 ± 0.2	(0.050 ± 0.008) 1.25 ± 0.2	(0.020 ± 0.012) 0.50 ± 0.30

Multilayer Chip

Surface Mount

ADMLIA Ferrite Series



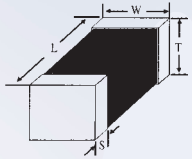
ADMLIA-2012FS

INTRODUCTION

The ADMLIA series are chip inductors widely used in the communication applications such as cellular phones, pagers, computers and other electronic devices. The device features in magnetic shielding which avoids cross coupling and crosstalk.

FEATURES

- Operating Temperature: -40°C to 85°C.
- Excellent solderability and resistance to soldering heat.
- Suitable for flow and reflow soldering.
- Good dimensions, high reliability, and easy surface mount assembly.
- 3 types of materials provide wide range of induction value for flexible needs.



SPECIFICATIONS

SIZE	LENGTH (A) (inch) mm	WIDTH (B) (inch) mm	THICKNESS (C) (inch) mm	TERMINAL (S) (inch) mm
ADMLIA-2012	(0.080 ± 0.008) 2.00 ± 0.2	(0.050 ± 0.008) 1.25 ± 0.2	(0.033 ± 0.008) 0.85 ± 0.2	(0.020 ± 0.012) 0.50 ± 0.30
ADMLIA-2012	(0.080 ± 0.008) 2.00 ± 0.2	(0.050 ± 0.008) 1.25 ± 0.2	(0.050 ± 0.008) 1.25 ± 0.2	(0.020 ± 0.012) 0.50 ± 0.30

ADMLIA-2012FS (0805) SERIES STANDARD SPECIFICATIONS

PACKAGE TYPE	INDUCTANCE ¹ (µH)	PERCENT TOLERANCE	Q ² min.	S.R.F. ³ min. (MHz)	RDC ⁴ max. (Ω)	IDC ⁵ max. (mA)
ADMLIA-2012FS-47N □ - T	0.047 @ 50 MHz	M	15 @ 50 MHz	320	0.20	300
ADMLIA-2012FS-68N □ - T	0.068 @ 50 MHz	M	15 @ 50 MHz	280	0.20	300
ADMLIA-2012FS-82N □ - T	0.082 @ 50 MHz	M	15 @ 50 MHz	255	0.20	300
ADMLIA-2012FS-R10 □ - T	0.10 @ 25 MHz	K,M	20 @ 25 MHz	235	0.30	250
ADMLIA-2012FS-R12 □ - T	0.12 @ 25 MHz	K,M	20 @ 25 MHz	220	0.30	250
ADMLIA-2012FS-R15 □ - T	0.15 @ 25 MHz	K,M	20 @ 25 MHz	200	0.40	250
ADMLIA-2012FS-R18 □ - T	0.18 @ 25 MHz	K,M	20 @ 25 MHz	185	0.40	250
ADMLIA-2012FS-R22 □ - T	0.22 @ 25 MHz	K,M	20 @ 25 MHz	170	0.50	250
ADMLIA-2012FS-R27 □ - T	0.27 @ 25 MHz	K,M	20 @ 25 MHz	150	0.50	250
ADMLIA-2012FS-R33 □ - T	0.33 @ 25 MHz	K,M	20 @ 25 MHz	145	0.55	250
ADMLIA-2012FS-R39 □ - T	0.39 @ 25 MHz	K,M	25 @ 25 MHz	135	0.65	200
ADMLIA-2012FS-R47 □ - T	0.47 @ 25 MHz	K,M	25 @ 25 MHz	125	0.65	200
ADMLIA-2012FS-R56 □ - T	0.56 @ 25 MHz	K,M	25 @ 25 MHz	115	0.75	150
ADMLIA-2012FS-R68 □ - T	0.68 @ 25 MHz	K,M	25 @ 25 MHz	105	0.80	150
ADMLIA-2012FS-R82 □ - T	0.82 @ 25 MHz	K,M	25 @ 25 MHz	100	1.00	150
ADMLIA-2012FS-1R0 □ - T	1.0 @ 10 MHz	K,M	45 @ 10 MHz	75	0.40	50
ADMLIA-2012FS-1R2 □ - T	1.2 @ 10 MHz	K,M	45 @ 10 MHz	65	0.50	50
ADMLIA-2012FS-1R5 □ - T	1.5 @ 10 MHz	K,M	45 @ 10 MHz	60	0.50	50
ADMLIA-2012FS-1R8 □ - T	1.8 @ 10 MHz	K,M	45 @ 10 MHz	55	0.60	50
ADMLIA-2012FS-2R2 □ - T	2.2 @ 10 MHz	K,M	45 @ 10 MHz	50	0.65	30
ADMLIA-2012FS-2R7 □ - T	2.7 @ 10 MHz	K,M	45 @ 10 MHz	45	0.75	30
ADMLIA-2012FS-3R3 □ - T	3.3 @ 10 MHz	K,M	45 @ 10 MHz	41	0.80	30
ADMLIA-2012FS-3R9 □ - T	3.9 @ 10 MHz	K,M	45 @ 10 MHz	38	0.90	30
ADMLIA-2012FS-4R7 □ - T	4.7 @ 10 MHz	K,M	45 @ 10 MHz	35	1.00	30
ADMLIA-2012FS-5R6 □ - T	5.6 @ 4 MHz	K,M	50 @ 4 MHz	32	0.90	15
ADMLIA-2012FS-6R8 □ - T	6.8 @ 4 MHz	K,M	50 @ 4 MHz	29	1.00	15
ADMLIA-2012FS-8R2 □ - T	8.2 @ 4 MHz	K,M	50 @ 4 MHz	26	1.10	15
ADMLIA-2012FS-100 □ - T	10 @ 2 MHz	K,M	50 @ 2 MHz	24	1.15	15
ADMLIA-2012FS-120 □ - T	12 @ 2 MHz	K,M	50 @ 2 MHz	22	1.25	15
ADMLIA-2012FS-150 □ - T	15 @ 1 MHz	K,M	30 @ 1 MHz	19	0.80	5
ADMLIA-2012FS-180 □ - T	18 @ 1 MHz	K,M	30 @ 1 MHz	18	0.90	5
ADMLIA-2012FS-220 □ - T	22 @ 1 MHz	K,M	30 @ 1 MHz	16	1.10	5
ADMLIA-2012FS-270 □ - T	27 @ 1 MHz	K,M	30 @ 1 MHz	14	1.15	5
ADMLIA-2012FS-330 □ - T	33 @ 0.4 MHz	K,M	30 @ 0.4 MHz	13	1.25	5

¹Inductance is measured in HP-4291B impedance analyzer with HP-16192 fixture. ²Q is measured in HP-4291B impedance analyzer with HP-16192 fixture.

³SRF is measured in HP-8753E RF network analyzer with HP-16192 fixture. ⁴RDC is measured in HP-4338B milliohmmeter. ⁵For 15°C Rise.

Multilayer Chip

Surface Mount

ADMLIA Ferrite Series — Continued

XTAL

OSC

VCXO
VCO

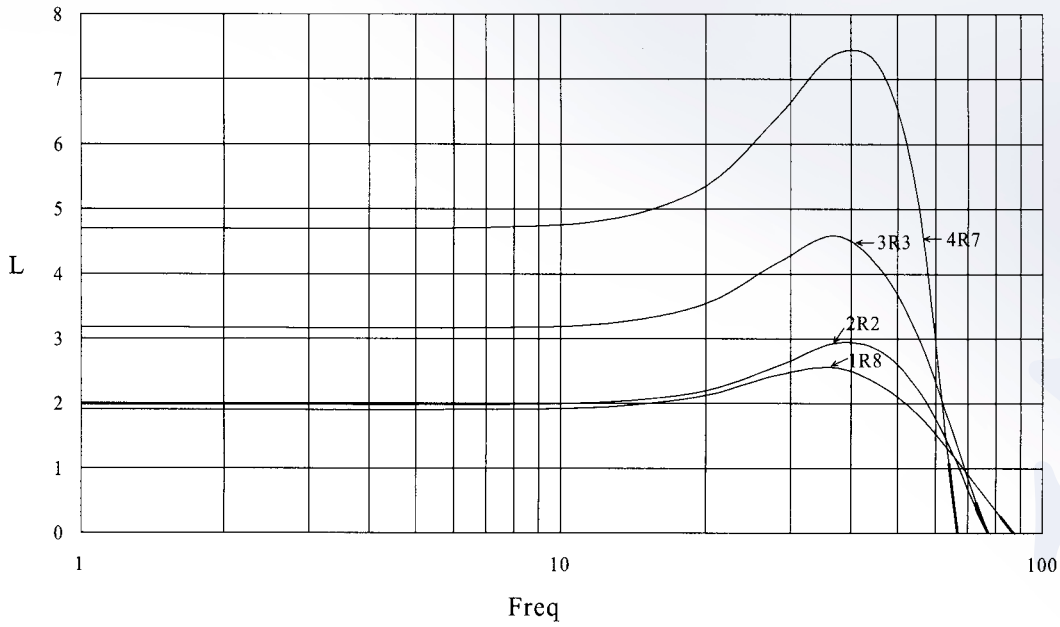
TCXO
VCTCXO

FLTR

RES

IND

ELECTRICAL CHARACTERISTIC
ADMLIA-2012FS (0805)



ADMLIA-2012FS (0805)

